

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10004121-1

SERIAL NO.

00/000,000

APPLICANT

Jeffrey G. Hargis, et al.

FILING DATE

Unknown

GROUP

Unk

11035 U.S. PTO
09/828041

04/07/01

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1A					
	1B					
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	"Preliminary Publication of JEDEC Semiconductor Memory Standards-DDR SDRAM Specification", August 1999 (73 page)
1R	JEDEC Standard No. 79, "Double Data Rate (DDR) SDRAM Specification", June 2000 (72 pages)
1S	

EXAMINER

H. Korn

DATE CONSIDERED

9/14/03



EV 156182939 US

PATENT APPLICATION

Sheet 1 of 1

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10004121-1

APPLICATION NO.

09/828,041

CONFIRMATION NO.

APPLICANT

Jeffrey G. Hargis et al.

FILING DATE

April 7, 2001

GROUP

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
<i>h</i>	1A	6,288,971 B1	09/11/2001	Kim	
	1B				
	1C				
	1D				
	1E				
	1F				
	1G				
	1H				
	1I				
	1J				
	1K				

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JAN 08 2003

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
<i>h</i>	1L	WO 99/04494	01/28/1999	Intel Corporation		
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	
	1R	
	1S	

EXAMINER

H Kim

DATE CONSIDERED

9/14/03